

Course Code	:	BCSL-022
Course Title	:	Assembly Language Programming Lab
Assignment Number	:	BCA(II)/L-022/Assignment/2024-25
Maximum Marks	:	50
Weightage	:	25%
Last Dates for Submission	:	31st October, 2024 (For July Session) 30th April, 2025 (For January Session)

This assignment has two questions of total of 40 marks. Rest 10 marks are for viva voce. Please go through the guidelines regarding assignments given in the programme guide for the format of presentation.

Q1. Design a two bit counter circuit that counts from 0 to 2. It should have states 00, 01 and 10. The initial state of the counter may be assumed to be 00. The counter will be in following successive states: 00, 01, 10, 00, 01, 10, 00, 01, 10, 00 ... Use J-K flip flop to design the circuit. You must design the circuit using state transition diagram and Karnaugh's maps. **(10 Marks)**

Q2. Write and run following programs using 8086 assembly language. **(3 × 10 = 30Marks)**

(a) Write and run an Assembly language program that converts a packed 4 digit BCD number that has been stored in two consecutive byte locations in the memory, into an equivalent binary number. The output should be stored in DX register. For example, if two consecutive byte locations have BCD values (12)_h and (34)_h then output will be binary equivalent of (1234)₁₀ which is (0000 0100 1101 0010)₂. This binary value will be stored in DX register.

(b) Write and run (using appropriate calling program) a near procedure in 8086 assembly language that checks if the input parameter has a value less than 5. If the value is less than 5 then subroutines displays the line "Parameter value is less than 5" else it displays "Parameter value is >= 5".

(c) Write and run an 8086 assembly language program that finds the factorial of the value stored in BH register. You may assume that BH register will store a maximum value 8. Thus, you need not handle the overflow.