

Assignment Booklet -1

**POST GRADUATE DIPLOMA IN INTELLECTUAL PROPERTY RIGHTS
(PGDIPR)**

Jan/July-2025

Tutor Marked Assignments (TMA)

For

MIP-101 to MIP 104



**SCHOOL OF LAW
INDIRA GANDHI NATIONAL OPEN UNIVERSITY
MAI DAN GARHI, NEW DELHI – 110 068**

Dear Student,

This booklet contains the assignments for MIP-101 to MIP-104.

Each course has one assignment, which you need to complete and send to your study centre before **April 30, 2025** for the students enrolled in the **January, 2025** session and **31st October, 2025** for the students enrolled in **July, 2025** session. Please note that, you will not be allowed to appear for the Term End Examination for the course, the assignments of which are not submitted before the due date.

The assignments constitute the continuous component of the evaluation process and have 30% weight age in the final grading. You need to score 40% marks in assignment for each course in order to clear the continuous evaluation component.

Instructions for Formatting Your Assignments

- 1) You should specify clearly on the first page of all your assignment responses the course code, course title, assignment code, your enrollment number, name and address in the given format:

	Enrolment no:
	Name:
	Address:
Course Code:.....	
Course Title:	
Assignment Code.....	
Regional Centre Code:.....	
Study Centre Code:.....	Date:.....

PLEASE FOLLOW THE ABOVE FORMAT STRICTLY TO FACILITATE EVALUATION AND TO AVOID DELAY.

- 2) The Assignment Response should be hand-written and use only A-4 or foolscap size writing paper (but not of very thin variety) for writing your answers.
- 3) Leave at least 4 cm margin on the left, top and bottom of your answer sheet for the evaluator's comments.
- 4) Your answers should be brief, precise and in your own words. **Please do not copy the answers from the study material.**
- 5) A serious action will be taken if any attempt to copy the Assignment Response from other student is done.
- 6) While solving the questions, clearly indicate the question number along with the part being solved. Recheck your work before submitting it.
- 7) This assignment will remain valid for **one year**. However, you are advised to submit it **within eight weeks** of receiving this booklet to accomplish its purpose as a teaching-tool.

We recommended that you retain a copy of your assignment response to avoid any unforeseen situation.

Our best wishes for your studies.

Programme Coordinator, PGDIPR

TUTOR MARKED ASSIGNMENT

MIP-103: Industrial Designs and Layout Designs of Integrated Circuits And Utility Models

Course Code: MIP-103

Assignment Code: MIP-101/TMA/2025

Maximum Marks: 100

Note:

- ☐ This assignment is based on **Block 1 to 4** of the course.
- ☐ All questions are compulsory.
- ☐ Please answers in your **own words**; do not copy from the course material.
- ☐ **Answer any Ten questions**

10X10= 100

1. What is the criteria for the registration of Industrial Design? Discuss
2. Distinguish between Industrial Design and a Patent with suitable examples.
3. Write briefly on the evolution of Industrial Design.
4. Write a note on the salient features of Designs Act, 2000. Discuss the essential requirements for the registration of 'design' under the Designs Act, 2000.
5. Explain the salient features of Semiconductor Integrated Circuits and Layout Design Act, 2000.
6. What is Utility Model? Discuss the benefits of the rights acquired under the Utility Model Protection.
7. Write a note on infringement of designs. Discuss case laws on infringement of designs.
8. Write a note on right to prevent unauthorized copying or imitation. Also discuss the rights to prevent unauthorized commercial transaction.
9. Illustrate some of the key inventions in IC. Explain your understanding on what is protected as an Intellectual Property?
10. Explain Reasons for protection of Industrial Design. How can Industrial Designs be protected?
11. Discuss the limitation of protections of IC through patents and copyrights. Give reasons.
12. What is the definition of the Semiconductor Integrated Circuit as per the SICLD Act, 2000? Enumerate the criteria for protection of chip layout design.